

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: AKASHI, Yuji, et al. Attention: Applications Division

Serial Number: 10/623,490 Group Art Unit: 2811

Filed: July 22, 2003 P.T.O. Confirmation No.: 6338

For: STACKED SEMICONDUCTOR DEVICE AND METHOD OF PRODUCING THE

**SAME** 

#### REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: January 8, 2004 Sir:

Please supply the undersigned attorney with a corrected filing receipt for the aboveidentified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the official Filing Receipt, we noted an error in that the foreign application information is incorrect and should read as -- JAPAN 2000-267621 9/4/2000--. A copy of the Declaration is enclosed which indicates the correct information. We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,

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Donald W. Hanson Attorney for Applicants Reg. No. 27,133

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PATENT TRADEMARK OFFICE

Enclosures: Official Filing Receipt; and Declaration



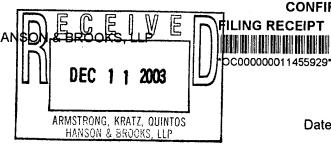
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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Viginia 22313-1450 www.uspto.gov

FILING OR 371 APPL NO ART UNIT FIL FEE REC'D ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS (c) DATE 10/623,490 07/22/2003 2811 880 . 010944A 26 . 10 1, ,

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**CONFIRMATION NO. 6338** 

Date Mailed: 12/10/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

#### Applicant(s)

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#### Assignment For Published Patent Application

FUJITSU LIMITED, Kawasaki, JAPAN;

## Domestic Priority data as claimed by applicant

This application is a DIV of 09/940,625 08/29/2001 PAT 6,621,169

## **Foreign Applications**

JAPAN 2002-267621 09/04/2000 -- JAPAN 2000 - 267621 9/4/2000 JAPAN 2001-121539 04/19/2001-

If Required, Foreign Filing License Granted: 12/08/2003

Projected Publication Date: 03/18/2004

Non-Publication Request: No

Early Publication Request: No

Title

Stacked semiconductor device and method of producing the same

**Preliminary Class** 

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